

Electronic Filing System (EFS) Data  
Electronic Patent Application Submission  
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EFS ID: 12148  
Application ID: 09682303  
Title of Invention: Delay Lock Circuit Using Bisection  
Algorithm and Related Method  
First Named Inventor: Jui-Lung Chen  
Domestic/Foreign Application: Domestic Application  
Filing Date: null  
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Submission Type: Utility Patent Filing  
Filing Type: new-utility  
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Attorney Docket Number: NAUP0303USA  
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Office, ou=Department of Commerce, o=U.S. Government, c=US  
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Deposit Account Number: 500801  
Deposit Account Name: WINSTON HSU



# TRANSMITTAL FORM

1c930 U.S. PTO  
09/682303  
08/16/01

Electronic Version 1.0.2

Stylesheet Version: 1.0

Attorney Docket Number:

NAUP0303USA

## Delay Lock Circuit Using Bisection Algorithm and Related Method

First Named Inventor: Jui-Lung Chen

### SUBMITTED BY

Name:

WINSTON HSU

Registration Number:

41,526

Electronic Signature Mark: Winston Hsu

Date Signed: 20010817

*I certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.*

*I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.*

### Attached Files:

bibd-transmittal	NAUP0303apds.xml
fee-transmittal	NAUP0303fee.xml
patent-assignment	NAUP0303asgn.xml
specification	NAUP0303.xml
declaration	NAUP0303DEC1.tif
declaration	NAUP0303DEC2.tif

### Attached Image File(s):

NAUP0303DEC1.tif

[illegible]

[illegible]



## Docket No : NAUP0303USA

As a below named inventor , I hereby declare that :

I believe I am the sole (if only one name appears below) , or joint (if more than one name appears) , original and first inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled :

## **"DELAY LOCK CIRCUIT USING BISECTION ALGORITHM AND RELATED METHOD"**

\_\_\_\_\_ + The specification for the above entitled invention is filed herewith.  
 \_\_\_\_\_ The specification for the above entitled invention was filed previously  
 with application serial number: \_\_\_\_\_ Filing Date: \_\_\_\_\_

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of the invention disclosed in this application in accordance with Title 37, Code of Federal Regulations, Section 1.56 (a). I further acknowledge the duty in any continuation-in-part application to disclose to the Patent and Trademark Office all information known to be material to the patentability of the invention disclosed in this application, as defined in 1.56, which became available to me between the filing date of the prior application and the filing date of this application.

**PRIORITY CLAIM**

  1   There is no claim of priority

       Claim of priority is based on the following:

\_\_\_\_\_

## POWER OF ATTORNEY

As a named inventor, I hereby appoint the following attorney to prosecute this application and to transact all related business in the Patent and Trademark Office:

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## DECLARATION

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued hereon.

Date: 8.14.01 Jui-Lung Chen

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# FEE TRANSMITTAL

Electronic Version 1.0.4

Stylesheet Version: 1.0

*Patent fees are subject to annual revisions on or about October 1st of each year.*

Large Entity

**TOTAL FEES AUTHORIZED: \$ 750**

The commissioner is hereby authorized to charge indicated processing and/or publication fees and credit any overpayments to:

Deposit Account Number: 500801

Deposit Account Name: NORTH AMERICA INTERNATIONAL PATENT OFFICE

Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.

## SUBMITTED BY

Authorized Name:

WINSTON HSU

Electronic Signature Mark:

VAEB-JMXX-8IIL

Date Signed:

20010814

## BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	101	\$ 710

Subtotal For Basic Filing Fee: \$ 710

## EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 15	103	\$ 18	0	\$ 0
Independent Claims: 2	102	\$ 80	0	\$ 0

Subtotal For Extra Claims Fees: \$ 0

ADDITIONAL FEES

Fee Description	Fee Code	Fee Paid
Recording Each Patent Assignment Per Property Fee	581	\$ 40

Subtotal For Additional Fees: \$ 40

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# APPLICATION DATA SHEET

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Attorney Docket Number: NAUP0303USA

Publication Filing Type: new-utility

Application Type: utility

Title of Invention: Delay Lock Circuit Using Bisection Algorithm and Related Method

Attorney Docket Number: NAUP0303USA

Customer Number Attorney: 027765



Customer Number Correspondence Address: 027765



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